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MENDMENT TRANSMITTAL LETTER After Final Rejection Application No. Filing Date Examiner Group Art Unit 09/993.094 November 6, 2001 P.T. Vu 2841 Docket No. Applicant(s): Shingo Matsumoto, Yasushi Fujita, and Katsumi Endo SONYJP 3.0-217 Invention: ADAPTER DEVICE, MEMORY DEVICE AND INTEGRATED CIRCUIT CHIP TO THE COMMISSIONER FOR PATENTS Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below. **CLAIMS AS AMENDED** Claims Highest Remaining Number Number After Previously **Extra Claims** Amendment Present Paid Rate **Total Claims** 9 9 Х Independent 5 5 Х Claims Multiple Dependent Claims (check if applicable) Other fee (please specify): TOTAL ADDITIONAL FEE FOR THIS AMENDMENT: 0.00 x Large Entity Small Entity x No additional fee is required for this amendment. Please charge Deposit Account No. in the amount of A duplicate copy of this sheet is enclosed. A check in the amount of \$ to cover the filing fee is enclosed. × The Commissioner is hereby authorized to charge and credit Deposit Account No. 12-1095 as described below. A duplicate copy of this sheet is enclosed. Credit any overpayment. Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17. Dated: February 14, 2005 Andrew T. Zidel Attorney Reg. No. 45,256 LERNER, DAVID, LITTENBERG, KRUMHOLZ & MENTLIK, LLP 600 South Avenue West Westfield, New Jersey 07090 (908) 518-6385

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

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_ (Andrew T. Zidel)

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EXPEDITED PROCEDURE

Group Art Unit: 2841

Docket No.: SONYJP 3.0-217

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

(Andrew T. Zidel)

In re Patent Application of:

Matsumoto et al.

Application No.: 09/993,094

: Group Art Unit: 2841

Filed: November 6, 2001

: Examiner: P.T. Vu

For: ADAPTER DEVICE, MEMORY DEVICE AND INTEGRATED CIRCUIT CHIP

REPLY PURSUANT TO 37 CFR § 1.116

MS AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed November 15, 2004, applicants submit the following amendments and remarks.